

wherein the converting unit converts the remainder portion data which is detected by the detecting unit, into a 4^{n-m} -byte serial data, where n is a natural number and m is a natural number selected from 1 to n.

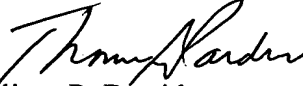
a¹ 16. (Amended) The data receiving device as claimed in claim 13, the parallel data is a variable-length data.

REMARKS

Claims 14 - 16 are pending. By this Preliminary Amendment, claims 14-16 are amended to change dependency. Prompt and favorable examination on the merits is respectfully requested.

The attached Appendix includes marked-up copies of each rewritten claim (37 C.F.R. 1.121(c)(1)(ii)).

Respectfully submitted,


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Attached: Appendix
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APPENDIX

Changes to Claims:

The following are marked-up versions of the amended claims:

14. (Amended) The data receiving device as claimed in claim 139, wherein the parallel data is a 4^n -byte parallel data, the predetermined number is 4^{n-m} , and the converting unit converts the remainder portion data into a 4-byte serial data, where n is a natural number and m is a natural number selected from 1 to n.

15. (Amended) The data receiving device as claimed in claim 139, wherein the CRC encoding circuit further comprises:

a detecting unit for detecting the remainder portion data from the last data set of the parallel data;

wherein the converting unit converts the remainder portion data which is detected by the detecting unit, into a 4^{n-m} -byte serial data, where n is a natural number and m is a natural number selected from 1 to n.

16. (Amended) The data receiving device as claimed in claim 139, the parallel data is a variable-length data.